WO 2006/037231 PCT/CA2005/001537

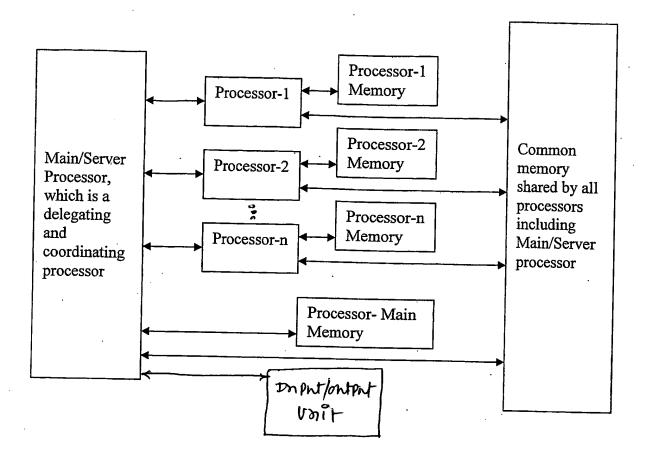


Fig. 4: Invented Parallel computer Architecture/organization

Marked-up copy of Fig. 4 of filed